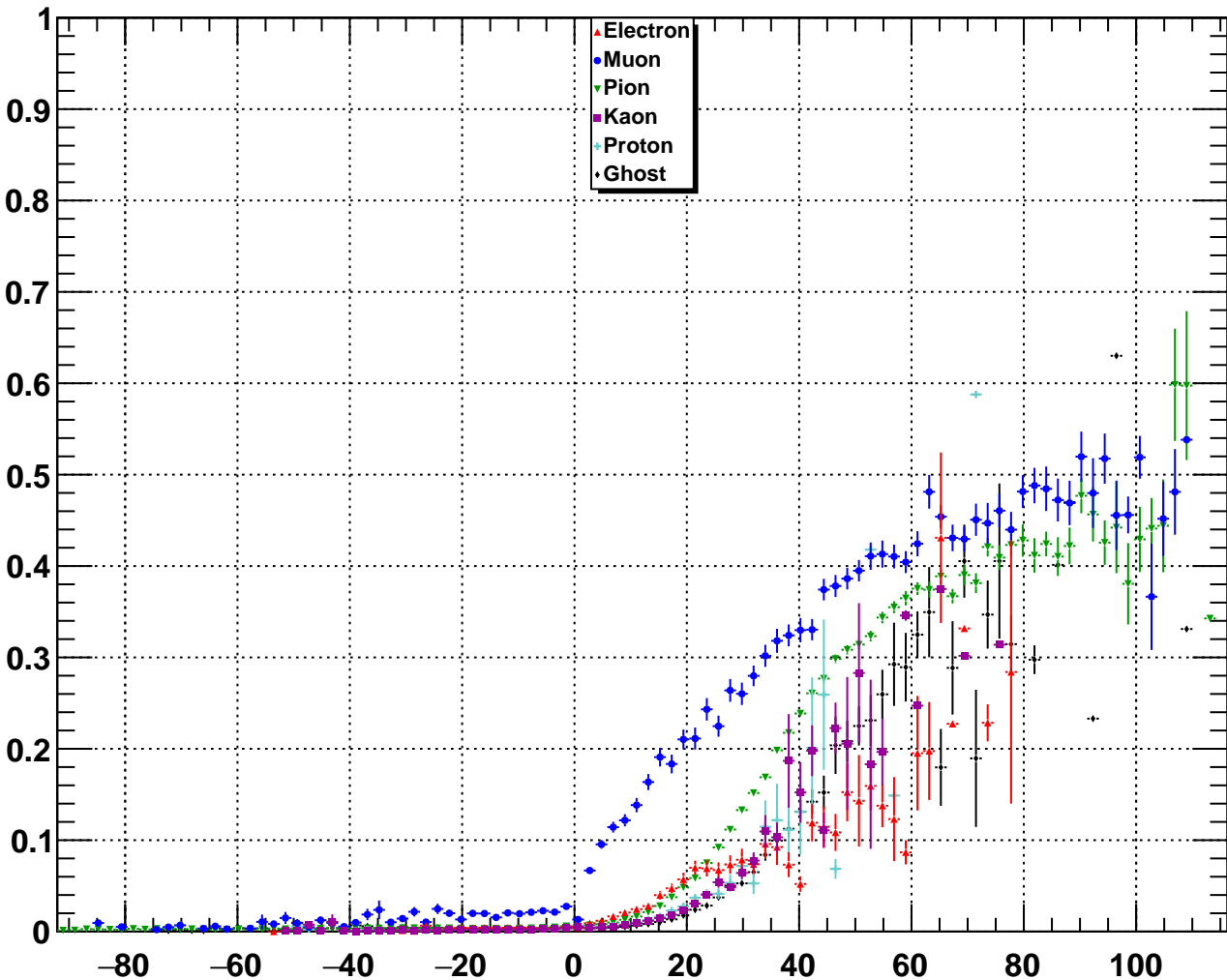


Upstream Muon MVA Output



Upstream Muon RichDLLmu