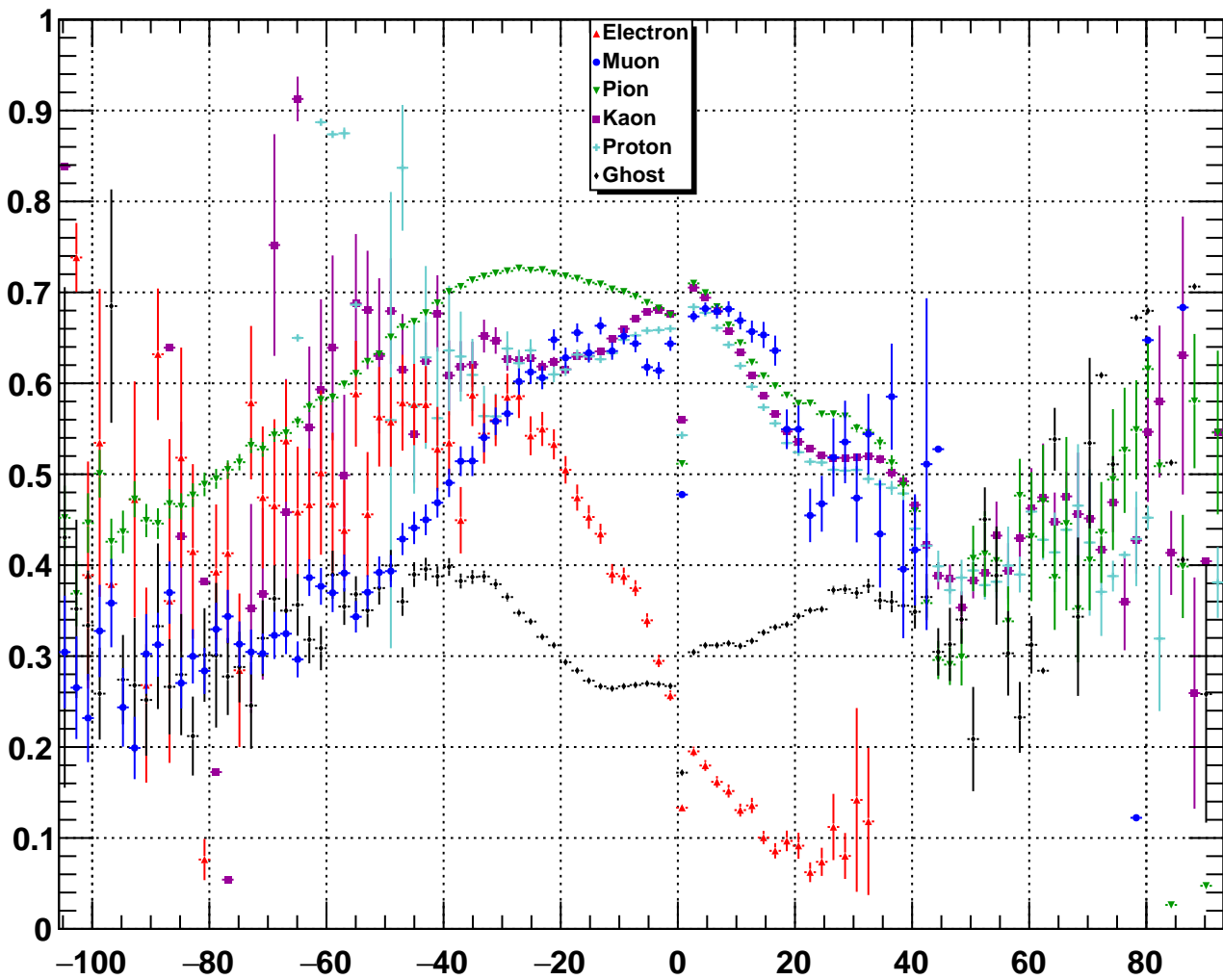


Upstream Pion MVA Output



Upstream Pion RichDLLp