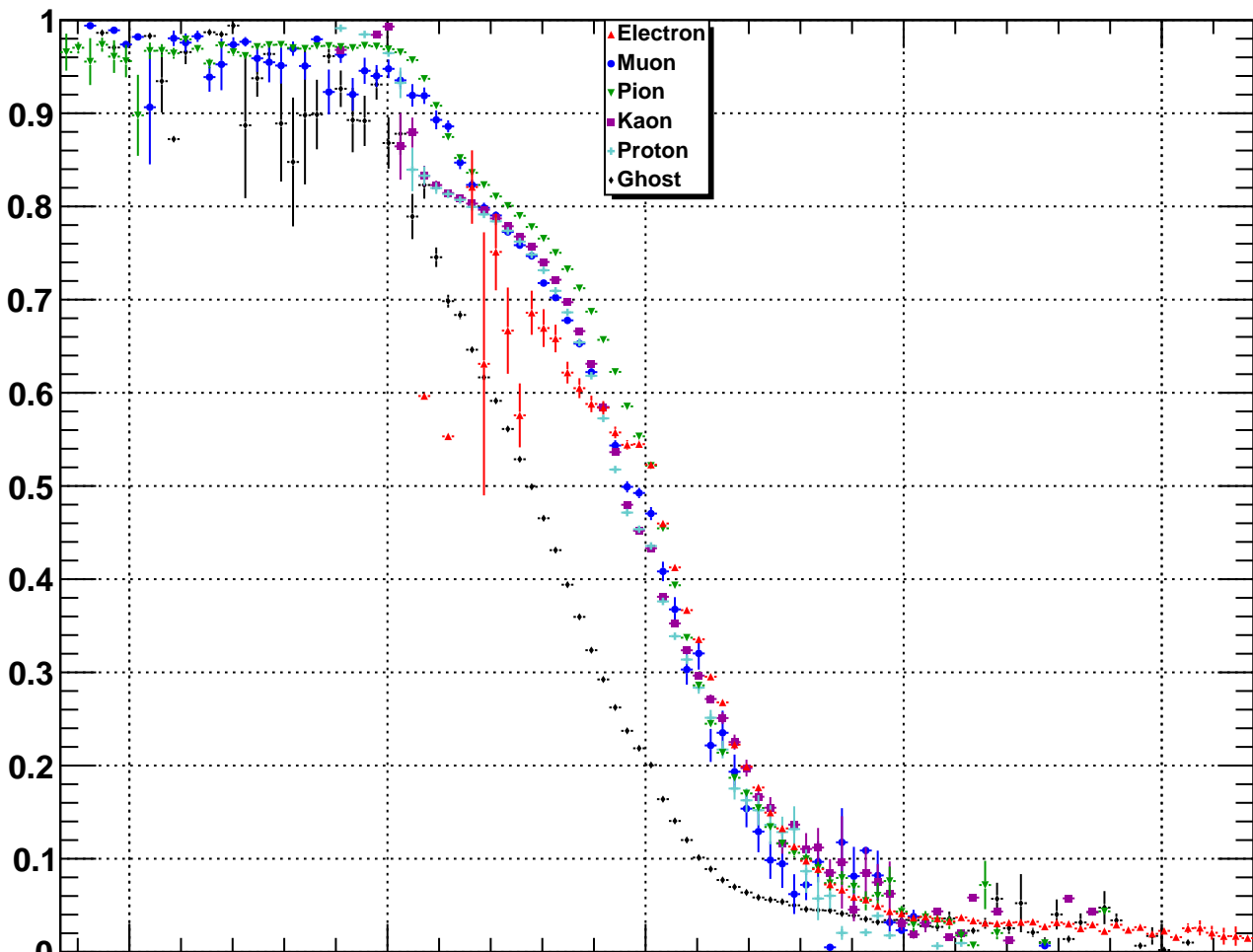


Upstream Pion MVA Output



Upstream Pion RichDLLe