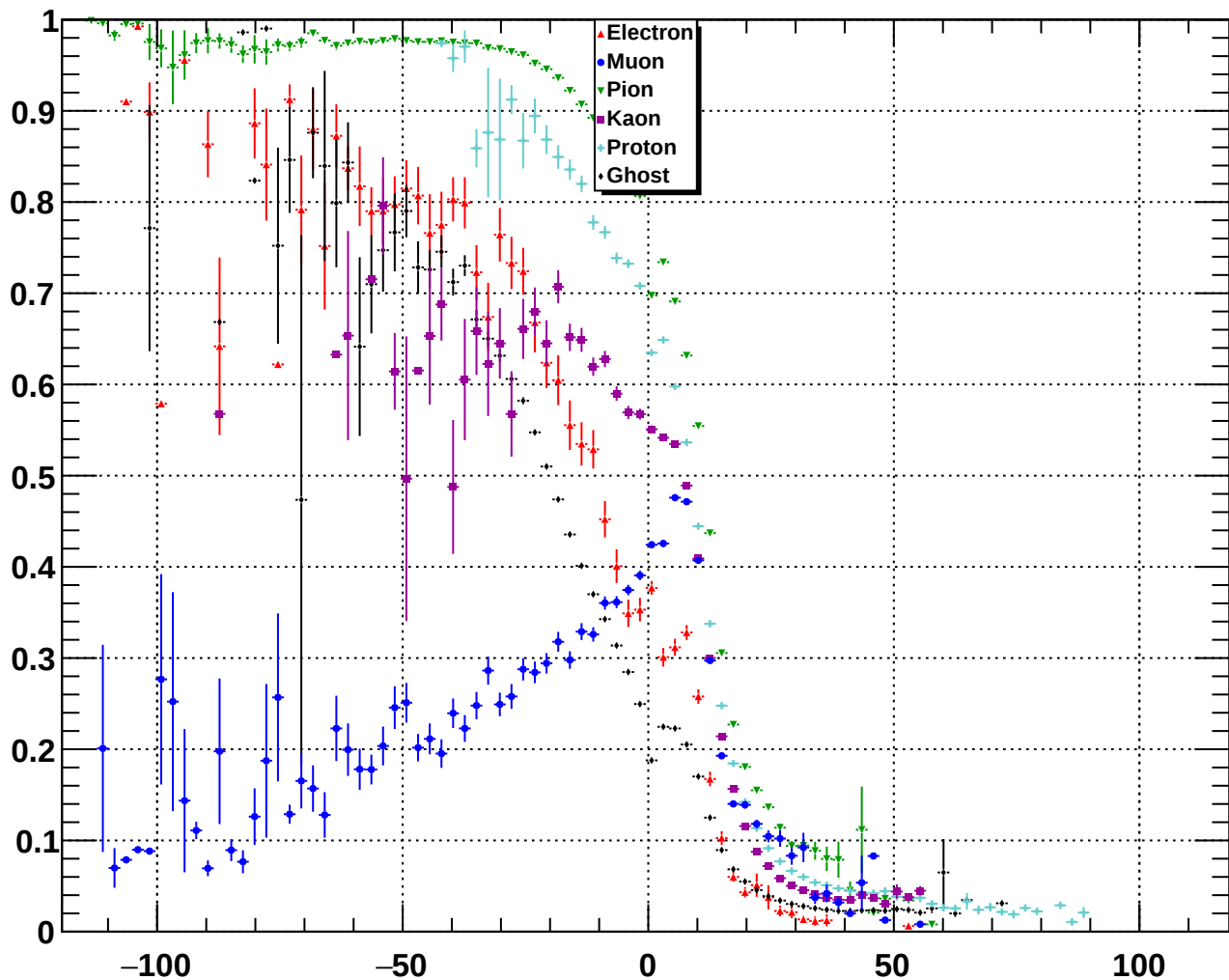


Downstream Pion MVA Output



Downstream Pion RichDLLp