

# Release 3 notes

This new release has several new features:

**first of all a panel that allows you to set up PVSS to use as many crates as you want.**

To do this:

Go to DP creation panel.

You can look at the 2 example lines already entered for you. For any additional Crate you want to add:

1) Usually you don't have to change the last 3 entries

2) change the first 3:

Crate Bus Node

2      0   elmb# ( the number you set on the ELMB)

3) press apply

4) then push the button create OPC Conf file: the OPC configuration file will be created in the panels directory: you have to copy it to the directory where your OPC server configuration file usually sits.

5) Then push the "add DPs and DPTs" button. Wait until it has finished

6) Push the "Create Addresses" button (this will take some time ~3 minutes for each additional crate)

7) Push the "set Conversion" button.

8) Push the "create Archive" button.

In this way you will create the OPCserver, the DP, the Addresses, the conversions and the archiving.

**The second major change wrt the previous version is in DataBase (DB) loading.**

A new format for the DataBase loading has been agreed with the DAQ group.

This DB is a human readable copy of the xml files created by the DAQ and the creation of those files is done by the DAQ itself.

Examples of the new DB files can be downloaded from this web page.

The database is at present sitting in the

C:\SCT\_DCS\Configs\Mops directory, so you should copy the DB files there after having created such a directory if you didn't have it already.

The configuration DB loading can be done by pressing the “load CNF DB” button in the LoadDB panel.

If you want to use it please check that the values in the DB fit to your purposes.

**A new version of the Crate Controller software is also available.**

This version corrects a bug in the status word of the LV card that is showing for Forward modules Termistor 0 as open, while it is termistor 0.

This was preventing the ELMB from ramping down the LV channels on a too high Temperature on termistor 0.